

REMARKS

The claims are claims 1 to 15 and 19 to 21.

Claims 1 to 18 were subject to a restriction requirement under 35 U.S.C. 121. The Examiner has identified the following groups:

I. Claims 1-15, a method of operating an integrated circuit including read/write accessible memory; and

II. Claims 16-18, an integrated circuit including a plurality of write once bits.

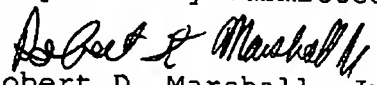
The Applicants elect the claims 1 to 15 of Group I without traverse. Non-elected claims 16 to 18 are cancelled.

New claims 19 to 21 are added. New claims 19 to 21 recite the step of operating the memory at a speed corresponding to the programmed write once bits. This subject matter is disclosed in the application at page 14, lines 3 to 7.

The Applicants respectfully request early consideration on the merits and advance to issue.

If the Examiner has any questions or other correspondence regarding this application, Applicants request that the Examiner contact Applicants' attorney at the below listed telephone number and address to facilitate prosecution.

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Respectfully submitted,

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